



PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

TANIGUCHI et al.

Art Unit: 2183

Application No.: 09/266,869

Examiner: Gautam R. Patel

Filed: March 12, 1999

Docket No.: 108075-09008

For: MICROCONTROLLER HAVING PREFETCH FUNCTION

12/A  
SMC  
5/16/02  
**RECEIVED**

MAY 16 2002

Technology Center 2600

AMENDMENT

Commissioner for Patents  
Washington, D.C. 20231

May 8, 2002

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MAY 13 2002

Technology Center 2100

Sir:

Please amend the application as follows:

IN THE CLAIMS:

Please amend claims 1, 8, 14 and 16 as follows:

A1 540 B1  
1. (Amended) A method for prefetching instructions and data of the program stored in a memory, wherein the program includes a pseudo instruction and at least one of an unconditional branch instruction, a conditional branch instruction, a CALL instruction, and a data calling instruction, the pseudo instruction being arranged before the at least one instruction and indicating that the at least one instruction or data follows the pseudo instruction, at least one instruction address or data address being part of the pseudo instruction, wherein the pseudo instruction is not executed, the method comprising the steps of:

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